

ÖZGEÇMİŞ

- 1. Adı Soyadı** : Ahmet Vedat Tavşanoğlu
İletişim Bilgileri
Adres : Mühendislik Fakültesi Elektrik-Elektronik Mühendisliği Bölümü
Telefon : 0535 5590486
Mail : vtavsanoglu@isikun.edu.tr
- 2. Doğum Tarihi** : 24.11.1945
- 3. Unvanı** : Prof. Dr.
- 4. Öğrenim Durumu** : Doktora

Derece	Alan	Üniversite	Yıl
Lisans	Elektronik ve Haberleşme	İTÜ	1968
Yüksek Lisans	Elektronik ve Haberleşme	İTÜ	1968
Doktora	Devreler ve Sistemler	Bath Üniversitesi, İngiltere	1974

5. Akademik Unvanlar

6. Yönetilen Yüksek Lisans ve Doktora Tezleri

6.1 Yüksek Lisans Tezleri

1. Cesur, E., “İki boyutlu HSA gabor filtresinin FPGA ile gerçekleştirilmesi”, 2006.
2. Yıldız, N., “Robotikte hücrel sinir ağları uygulamaları”, 2007.
3. Alpay, M., ”Yön seçici ve hıza ayarlı filtreler, Gauss ve Gabor filtrelerinde son gelişmeler”, 2007.
4. Polat, A., “Hücrel sinir ağları ve yön seçici Gabor süzgeçleri”, 2007.
5. Özüntürk, E., “Optik akış ile hareket tespiti”, 2007.
6. Korkusuz, N.G., “Otomatik hedef tanıma (ATR)”, 2008.
7. Özcan, A.R., “Gömülü yaya belirleme sistemi tasarımı ve gerçekleştirilmesi”, 2013.
8. Çalık, N., “Sayısal donanım gerçekleştirilmesine yönelik el yazısı karakter tanıma sistemi” 2013.
9. Aldemir, E., “Bir otomatik hedef tanıma algoritmasının donanım gerçekleştirilmesine yönelik optimizasyonu”, 2013.

6.2. Doktora Tezleri

1. Thiele, L. "Zur Approximation und Realisierung Elektrischer Netzwerke mit Methoden der Linearen Algebra", Mnnih Teknik Universitesi, 1985
2. Joy, M. P.: "On the Convergent Dynamics of Cellular Neural Networks", Londra South Bank Universitesi, 1996
3. Tufan, E.: "Multisubband Structures and their Application to Image Processing", Londra South Bank Universitesi, 1996
4. Arık, S.: "Stability Analysis of Dynamical Neural Networks", Londra South Bank Universitesi, 1997
5. Saati, E.: "Application of CNN to Image Processing", Londra South Bank Universitesi, 2003
6. Kayaer, K.: "Gerek Zamanlı Video İřleyen Yeni Bir Hcresel Sinir Ađı Emlatr Tasarımı ve FPGA ile Gereklenmesi", Yıldız Teknik niversitesi, 2008
7. Polat, N.: "Uzaysal ve Uzay-Zamansal HSA Filtreleri", Yıldız Teknik niversitesi, 2009
8. Daniř, . A.: "Zamanlama ve Anahtarlama Mimarilerinin Tasarımı ve Donanımsal Gereklenmesi", Yıldız Teknik niversitesi, 2010
9. Cesur, E.: "Gerek Zamanlı bir Hcresel Sinir Ađı Yapısının Tasarımı ve bu Yapıyla Gabor Filtrelerinin FPGA zerinde Gereklenmesi", Yıldız Teknik niversitesi, 2012
10. Yıldız, N.: "Design and Implementation of a Cellular Neural Network Emulator for High Resolution Video Image Processing", Yıldız Teknik niversitesi, 2012
11. Alpay, M.: "Design of a Multi-Layer Cellular Neural Network Emulator and Its Implementation on FPGA", Yıldız Teknik niversitesi, Mayıs 2013.
12. Yavuz, O.: "Zaman Trevli Hcresel Sinir Ađları ve Grnt İřleme Uygulamaları", Yıldız Teknik niversitesi, Mayıs 2013.

7. Yayınlar

7.1. Uluslararası hakemli dergilerde yayınlanan makaleler

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- 2 **Tavsanoglu, V.:** "Simple Evaluation of the Matrix T that Relates two Equivalent Foster Networks", **Electronics Letters**, vol.9, no.18, pp.422-424, 6 Sept. 1973.
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- 15 Finger, L., and **Tavsanoglu, V.:** "Mapping of One-dimensional Josephson Junction Arrays onto Cellular Neural Networks and their Dynamics", **IEEE Trans. Circuits Syst.-Part I**, vol.44, No.5, pp.438-445, May 1997.
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7.2. Uluslararası bilimsel toplantılarda sunulan ve bildiri kitabında (Proceeding) basılan bildiriler.

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 - 4 **Tavsanoglu, V.:** "Sensitivity Comparison of Some Second-Order Active-RC Low-Pass Filter Networks: State-Space Approach", **Proc.1980 IEEE International Symposium on Circuits and Systems (ISCAS'80)**, pp.793-796, Houston, Texas, U.S.A., 28-30 Apr.1980.
 - 5 **Tavsanoglu, V.:** "On the Reduction of Sensitivities in the State-Variable Realisations of Second-Order Transfer Functions", **Proc. 1980 European Conference on Circuit Theory and Design (ECCTD'80)**, pp. 596-600, Warsaw, Poland, 2-5 Sept. 1980.
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European Conference on Circuit Theory and Design (ECCTD'81), pp. 972-977, The Hague, The Netherlands, 25-28 Sept. 1981.

- 8 **Tavsanoglu, V.**, and Thiele, L.: "Simultaneous Minimisation of Roundoff Noise and Sensitivity in State-Space Digital Filters", **Proc. 1983 IEEE International Symposium on Circuits and Systems (ISCAS'83)**, pp.815-818, Newport Beach, Cal., U.S.A., 2-4 May 1983.
- 9 **Tavsanoglu, V.**: "Simple Evaluation and Optimisation of Gain-Sensitivity Products in a Class of Active-RC Circuits: A State-Space Approach", **Proc. 1983 European Conference on Circuit Theory and Design (ECCTD'83)**, pp.160-163, Stuttgart, F.R.G., 5-9 Sept.1983.
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- 13 **Tavsanoglu, V.**: "The Necessary and Sufficient Conditions for Minimum Sensitivity in Second-Order State-Space Digital Filters", **Proc. 6th Int. Symp. Networks, Syst, Signal Processing (ISYNT'89)**, pp.48-52, Zagreb, Yugoslavia, 27-29 June 1989.
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- 18 Joy, M.P., and **Tavsanoglu, V.**: "Perturbations of CNNs", **Proc. 1994 IEEE International Symposium on Circuits and Systems (ISCAS'94)**, vol.6, pp.221-224, London, UK, 30 May- 2 June 1994.
- 19 Joy, M. P., and **Tavsanoglu, V.**: "Circulant Matrices and the Stability Theory of CNNs", **Proc. 1994 Third IEEE International Workshop on Cellular Neural Networks and their Applications (CNNA-94)**, pp. 21-26, Rome, Italy, 18-21 December 1994.
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- 24 Tufan, E., and **Tavsanoglu, V.**: “Design of Two-Channel IIR PRQMF Banks Through Balanced Model Reduction”, **Proc. 1995 European Conference on Circuit Theory and Design (ECCTD’95)**, vol.1, pp.179-182, Istanbul, Turkey, 27-31 August
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7.3. Yazılan Uluslar arası kitaplar veya kitaplarda bölümler.

7.4. Ulusal hakemli dergilerde yayınlanan makaleler

7.5. Ulusal bilimsel toplantılarda sunulan bildiri kitabında basılan bildiriler

1. Kayaer, K., Tavşanoğlu, V., “Gerçek Zamanlı Video İşleyen Yeni Bir Hücresel Sinir Ağı Emülatörü Yapısı” SİU 2009, Antalya, 09-11 Nisan 2009
2. Yıldız, N., Cesur, E., Tavşanoğlu, V., “İkinci Nesil Gerçek Zamanlı HSA İşlemcisinin Tanıtımı: RTCNNP-V2”, SİU 2011, Antalya, 20-22 Nisan 2011
3. Yıldız, N., Cesur, E., Tavşanoğlu, V., “Gabor Benzeri HSA Filtrelerinin FPGA ile Gerçeklenmesinin,” SİU 2011, Antalya, 20-22 Nisan 2011
4. Aldemir, E., Yıldız, N., Tavşanoğlu, V., “Bir Otomatik Hedef Tanıma Algoritmasının Geliştirilmesi”, SİU 2013, Girne, KKTC, 23-25 Nisan 2013
5. Çalık, N., Cesur, E., Tavşanoğlu, V., “Hücresel Sinir Ağları Kullanılarak El Yazısı Tanıma Uygulaması” SİU 2013, Girne, KKTC, 23-25 Nisan 2013
6. Tavşanoğlu, V., “Dirençsel Izgara Görüntü Filtrelerinin FPGA İle Gerçekleştirilmeleri İçin Jacobi Yinelemesi Yöntemi İle Benzetimi”, SİU 2014, Trabzon, 23-25 Nisan 2014

7.6 Diğer Yayınlar

8.Projeler

108E023 Sayılı TÜBİTAK Projesi (2007-2010)

9.İdari Görevler

10.Bilimsel Kuruluşlara Üyelikleri

IEEE

11.Ödüller

1981 TÜBİTAK Teşvik Ödülü

12.Son iki yılda verdiği lisans ve lisansüstü düzeyindeki dersler

Akademik Yıl	Dönem	Dersin Adı	Haftalık Saati		Öğrenci Sayısı
			Teorik	Uygulama	
2012-2013	Bahar	Dijital Süzgeçler*	3	0	5
2012-2013	Bahar	Görüntü İşleme*	3	0	3
2013-2014	Güz	İleri Dijital İşaret İşleme*	3	0	8
2013-2014	Güz	Görüntü İşleme*	3	0	5
2013-2014	Bahar	İleri Görüntü İşleme*	3	0	2
2013-2014	Bahar	Dijital Süzgeçler*	3	0	2
2014-2015	Güz	İleri Dijital İşaret İşleme*	3	0	5
2014-2015	Güz	Görüntü İşleme*	3	0	3

* İşaretli dersler, yüksek lisans dersleridir.